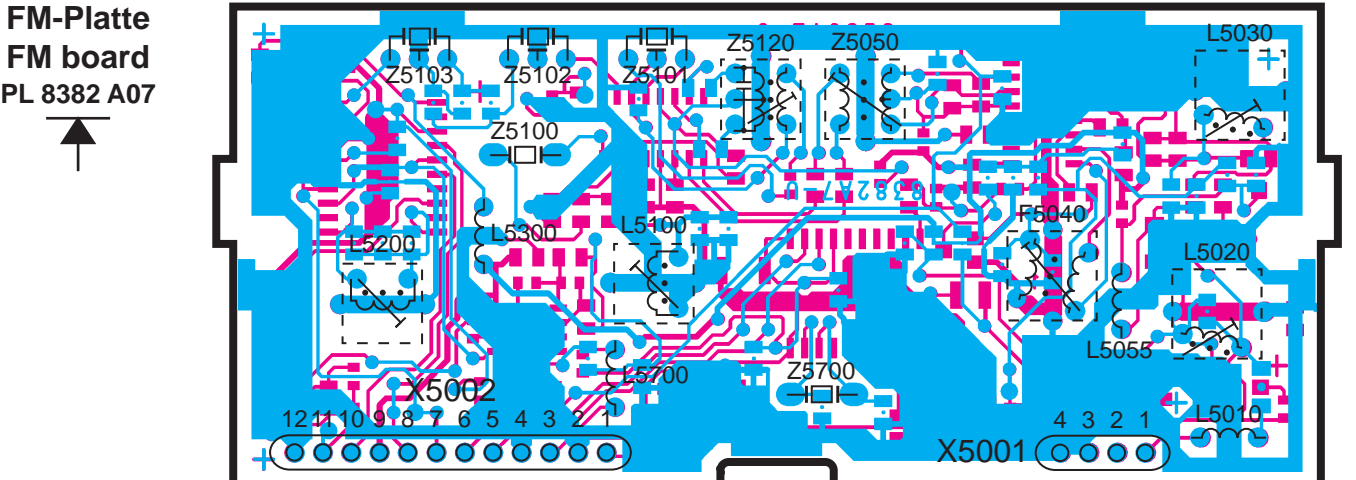
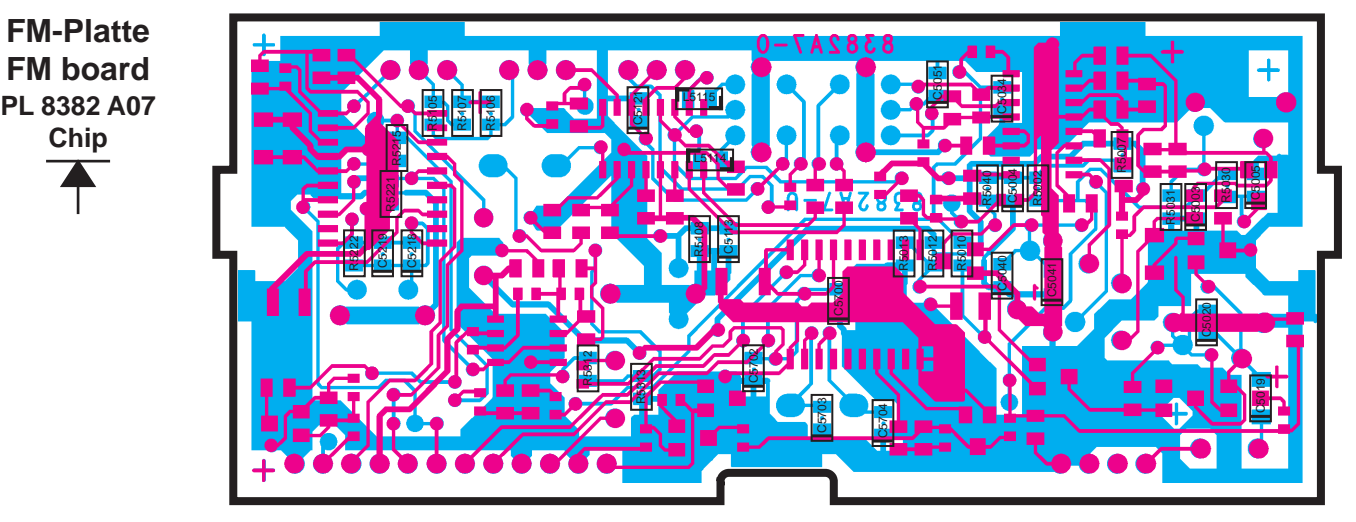
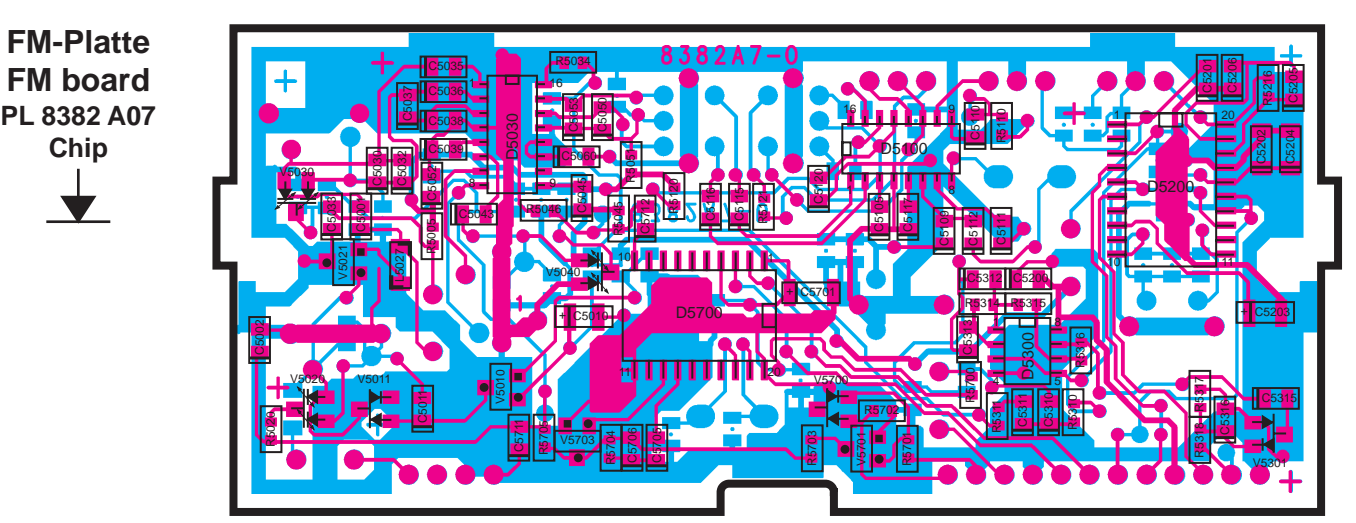
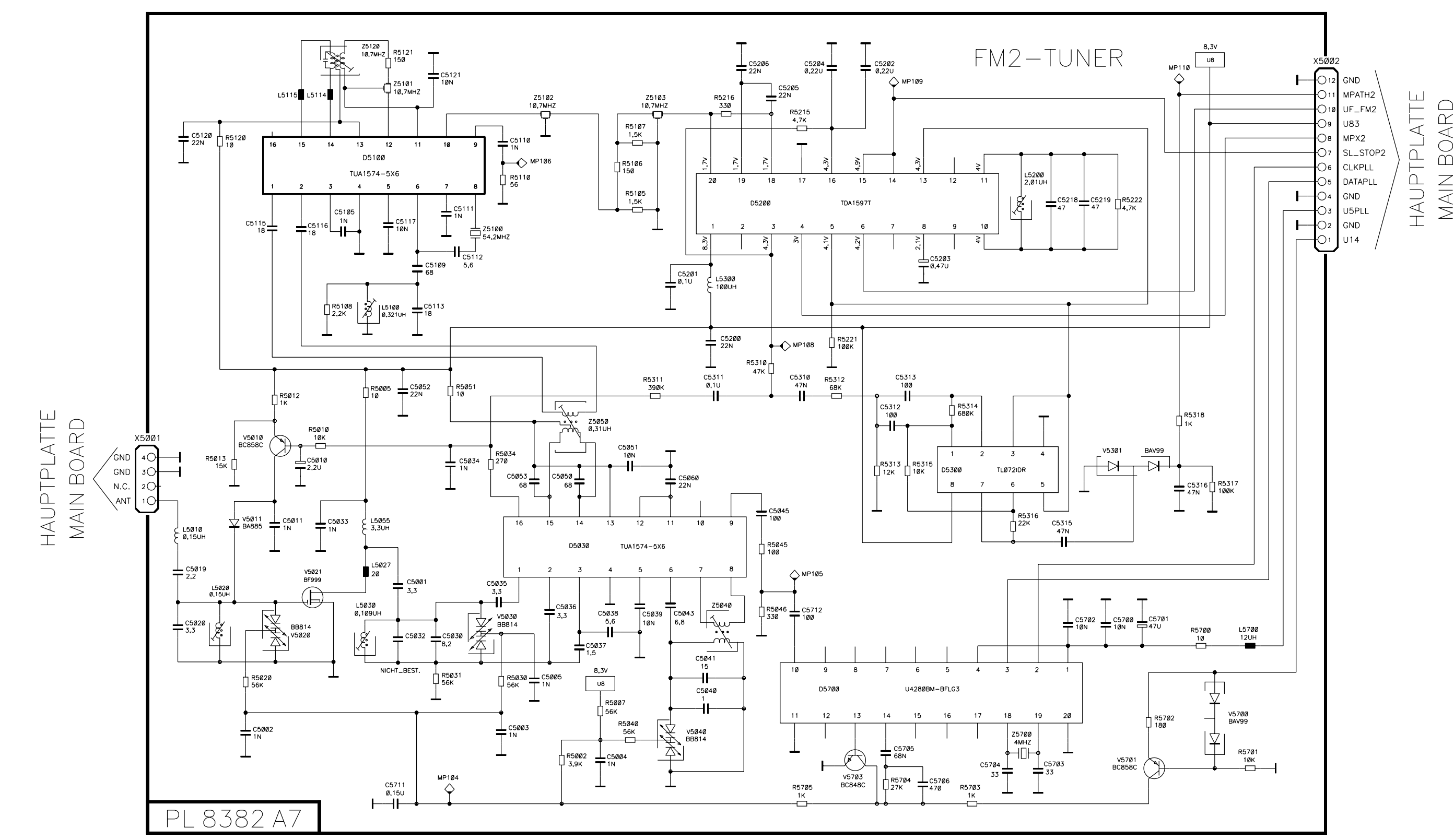
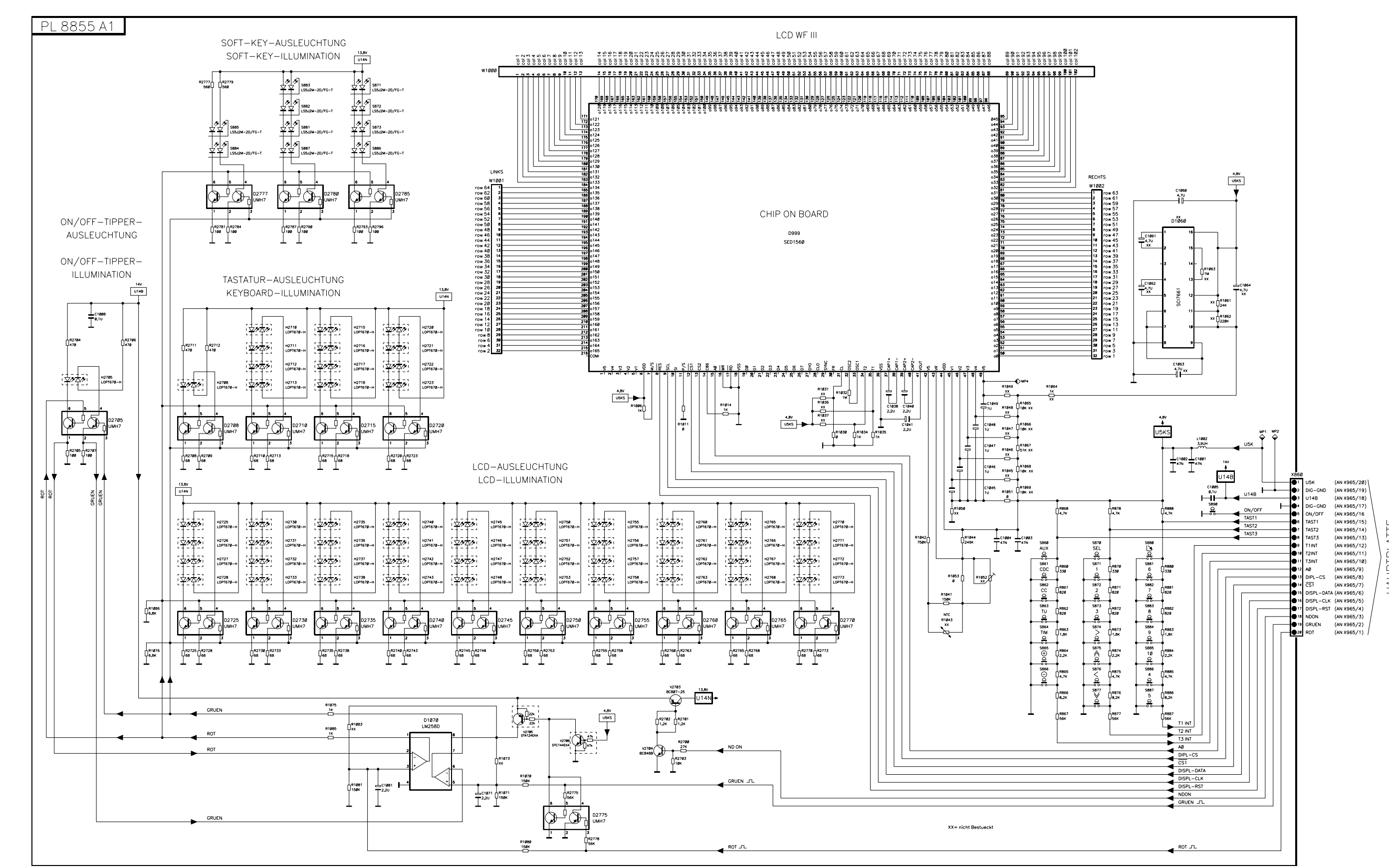
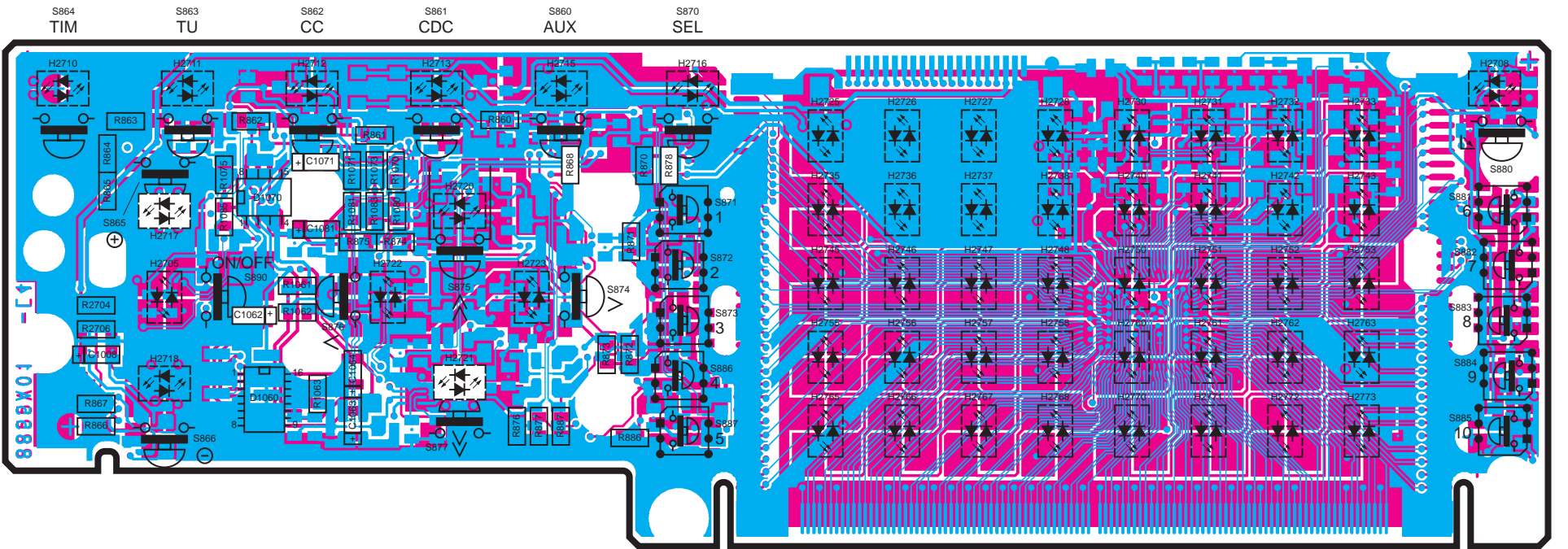
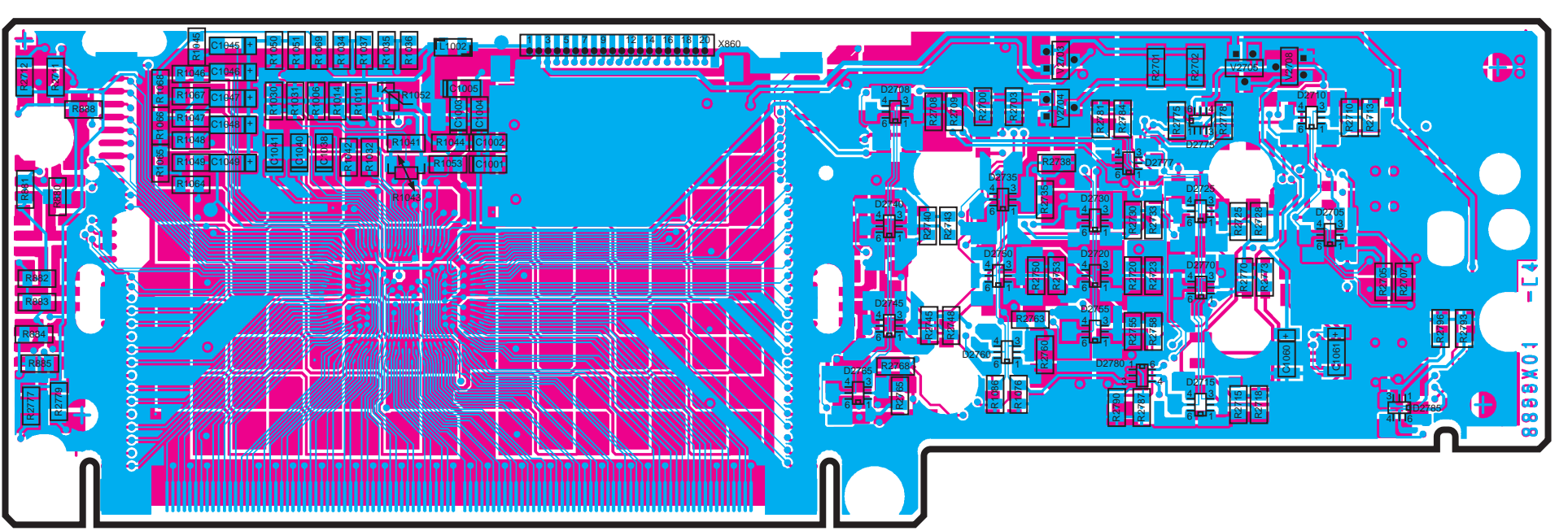
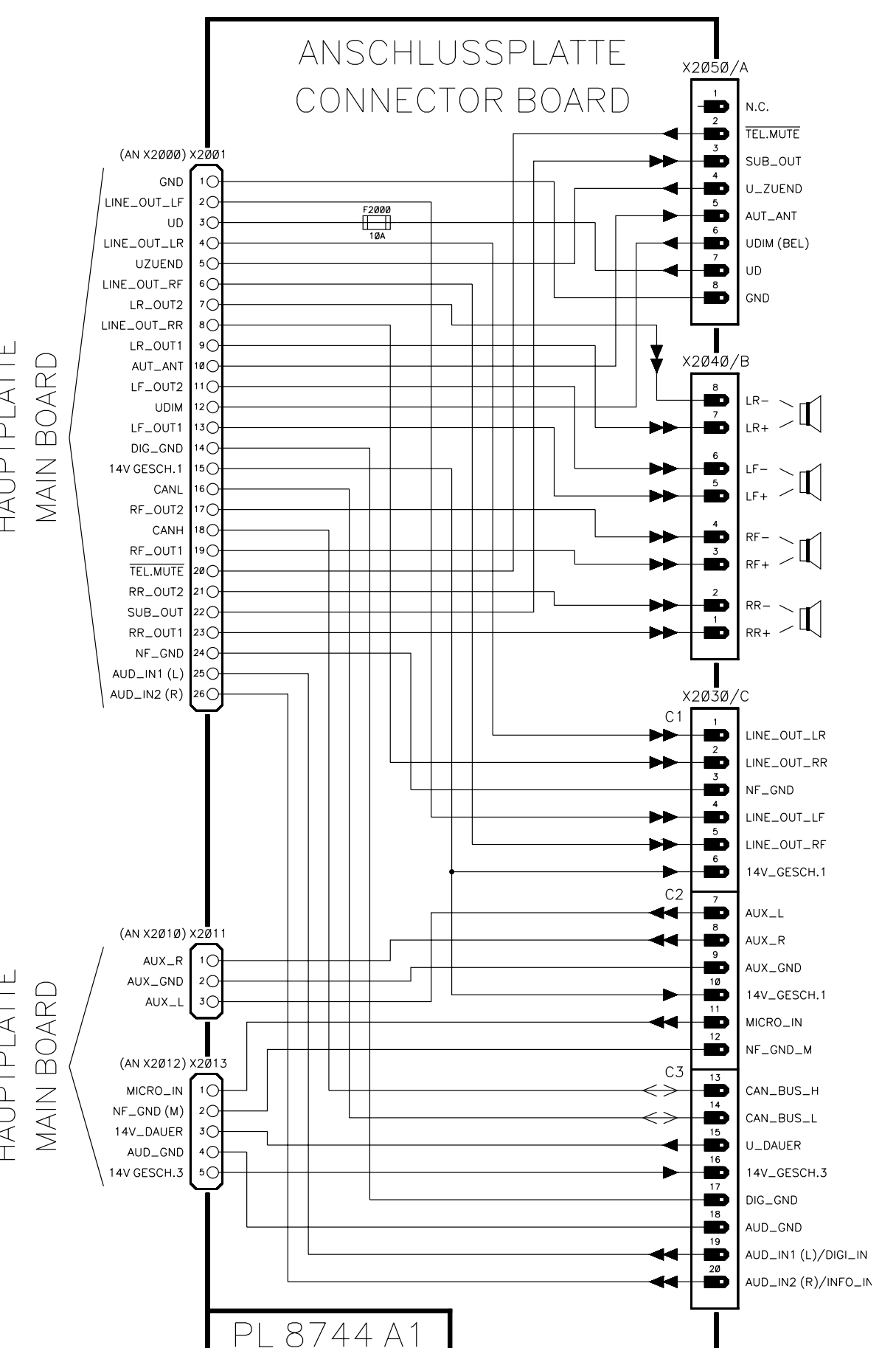


X12			
1 = FM_GND	5 = UREF	9 = MPATH1	13 = U14
2 = U81	6 = UF_FM1	10 = NF_AM	14 = AM_GND
3 = UF_UNBEW.	7 = SL_STOP1	11 = UF_AM	15 = PLL_GND
4 = NF_FM1	8 = SLFast	12 = U82	16 = AM / FM
X11			
1 = HF_GND	2 = ANT	3 = U85	4 = U81

X860			
1 = U5K	6 = TAST1	11 = T3_INT	16 = DISPL_CLK
2 = DIG_GND	7 = TAST2	12 = A0	17 = DISPL_RST
3 = U14B	8 = TAST3	13 = DIPL_CS	18 = NDOH
4 = DIG_GND	9 = T1_INT	14 = CS1	19 = GRUEN
5 = ON/OFF	10 = T2_INT	15 = DISPL_DATA	20 = ROT

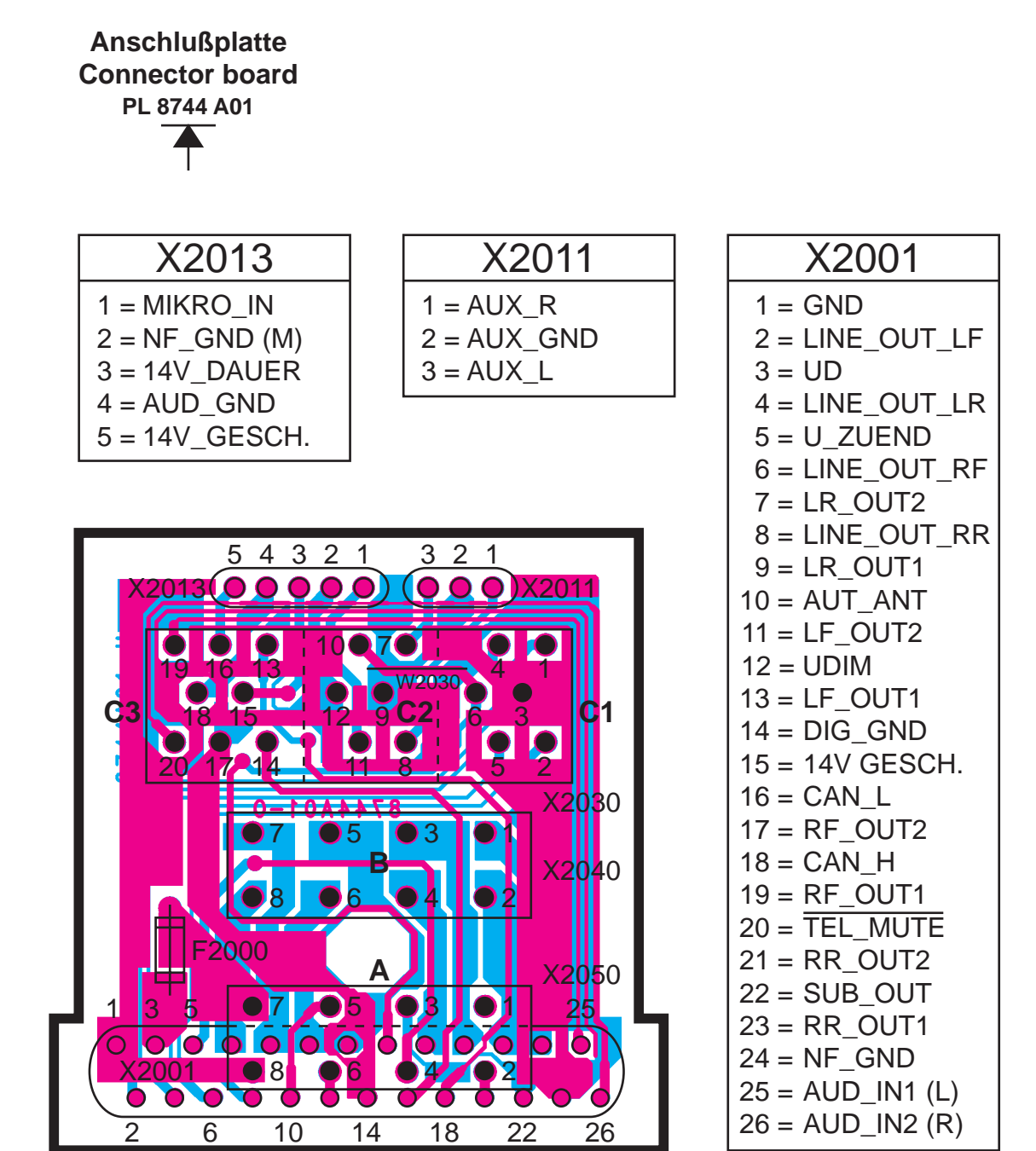


X5002			
1 = U14	5 = DATAPLL	9 = U83	13 = CLKPLL
2 = GND	6 = CLKPLL	10 = UF_FM2	14 = MPATH2
3 = USPLL	7 = SL_STOP2	11 = MPATH2	15 = GND
4 = GND	8 = MPX2	12 = GND	
X5001			
1 = ANT	2 = N/C	3 = GND	4 = GND

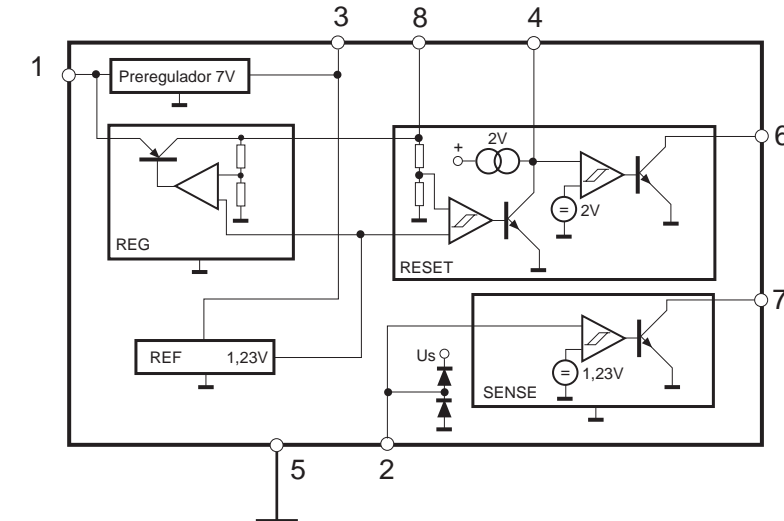


BLAUPUNKT AUTORADIO Bremen RCM 127 7 646 860 010
New York RDM 127 7 646 870 010

Schaltbild • Circuit diagramm • Schema du poste • Esquema del aparato



X2013			
1 = MIKRO_IN	2 = NF_GND (M)	3 = 14V_DAUER	4 = AUD_GND
5 = 14V_GESCH.			
X2011			
1 = AUX_R	2 = AUX_GND	3 = AUX_L	
X2001			
1 = GND	2 = LINE_OUT_LF	3 = UD	4 = LINE_OUT_LR
5 = U_ZUEND	6 = LINE_OUT_RF	7 = LR_OUT2	8 = LINE_OUT_RR
9 = LR_OUT1	10 = AUT_ANT	11 = LF_OUT2	12 = UDIM
13 = LF_OUT1	14 = DIG_GND	15 = 14V_GESCH.	16 = CAN_L
17 = RF_OUT2	18 = CAN_H	19 = RF_OUT1	20 = TEL_MUTE
21 = RR_OUT2	22 = SUB_OUT	23 = RR_OUT1	24 = NF_GND
25 = AUD_IN1 (L)	26 = AUD_IN2 (R)		



Hauptplatte
Main board
PL 8442 A03

- X12
- 1 = GND
 - 2 = U81
 - 3 = UF_UNBEW.
 - 4 = NF_FM1
 - 5 = UREF
 - 6 = UF_FM1
 - 7 = SL_STOP1
 - 8 = SLFast
 - 9 = MPATH1
 - 10 = NF_AM
 - 11 = UF_AM
 - 12 = U82
 - 13 = U14
 - 14 = GND
 - 15 = GND
 - 16 = AM / FM
 - 17 = U5PLL
 - 18 = CLK_PLL
 - 19 = DATA_PLL

- X11
- 1 = GND
 - 2 = N.C.
 - 3 = ANT
 - 4 = N.C.
 - 5 = U85
 - 6 = U81

- X5001
- 1 = ANT
 - 2 = N.C.
 - 3 = GND
 - 4 = GND

- X5002
- 1 = U14
 - 2 = GND
 - 3 = U5PLL
 - 4 = GND
 - 5 = DATAPLL
 - 6 = CLKPLL
 - 7 = SL_STOP2
 - 8 = MPX2
 - 9 = U83
 - 10 = UF_FM2
 - 11 = MPATH2
 - 12 = GND

- X2010
- 1 = AUX_R
 - 2 = AUX_GND
 - 3 = AUX_L

- X2000
- 1 = GND
 - 2 = LINE_OUT_LF
 - 3 = UD
 - 4 = LINE_OUT_LR
 - 5 = U_ZUEND
 - 6 = LINE_OUT_RF
 - 7 = LR_OUT2
 - 8 = LINE_OUT_RR
 - 9 = LR_OUT1
 - 10 = AUT_ANT
 - 11 = LF_OUT2
 - 12 = UDIM
 - 13 = LF_OUT1
 - 14 = DIG_GND
 - 15 = 14V GESCH.
 - 16 = CAN_L
 - 17 = RF_OUT2
 - 18 = CAN_H
 - 19 = RF_OUT1
 - 20 = TEL_MUTE
 - 21 = RR_OUT2
 - 22 = SUB_OUT
 - 23 = RR_OUT1
 - 24 = NF_GND
 - 25 = AUD_IN1 (L)
 - 26 = AUD_IN2 (R)

- X2012
- 1 = MIKRO_IN
 - 2 = NF_GND (M)
 - 3 = 14V_DAUER
 - 4 = AUD_GND
 - 5 = 14V_GESCH.

- X1300/CC-LW
- 1 = CS2
 - 2 = DATA_IN
 - 3 = DATA_OUT
 - 4 = CLK
 - 5 = CS1
 - 6 = HALL2
 - 7 = HALL1
 - 8 = U5
 - 9 = U14
 - 10 = U14
 - 11 = GND
 - 12 = GND
 - 13 = OE
 - 14 = U85
 - 15 = U85
 - 16 = TB_L
 - 17 = NF_GND
 - 18 = TB_R
 - 19 = NF_GND
 - 20 = CPS_OUT

- X2080
- 1 = MOTOR -
 - 2 = MOTOR +

Hauptplatte
Main board
PL 8442 A03
Chip

- X5000/CD-LW
- 1 = INSW
 - 2 = EMPHCD
 - 3 = DSP/ON
 - 4 = LRCKCD
 - 5 = DATACD
 - 6 = BCKCD
 - 7 = XTA/INCD
 - 8 = GND
 - 9 = GND
 - 10 = GND
 - 11 = U5
 - 12 = U5
 - 13 = U5
 - 14 = RESET
 - 15 = SLEEP
 - 16 = CS
 - 17 = SCK
 - 18 = D_CD_MS
 - 19 = D_MS_CD
 - 20 = SRQ

- X965
- 1 = ROT
 - 2 = GRUEN
 - 3 = NDON
 - 4 = DISPL_RST
 - 5 = DISPL_CLK
 - 6 = DISPL_DATA
 - 7 = CS1
 - 8 = DIPL_CS
 - 9 = A0
 - 10 = T3_INT
 - 11 = T2_INT
 - 12 = T1_INT
 - 13 = TAST3
 - 14 = TAST2
 - 15 = TAST1
 - 16 = ON/OFF
 - 17 = DIG_GND
 - 18 = U14B
 - 19 = DIG_GND
 - 20 = U5K

	DTA1432KA	DTC144EKA	DTA1424EKA	BC848	BC848
	V2002	V301	V346	V324	V241
	V2003	V330	V2048	V332	V2770
	V2004	V1919	V2796	V347	V2781
	V2005	V2012	V5241	V831	
	V2006	V2013		V846	
	V2007	V2006	DTA1432KA	V1665	V2218
	DTA124EKA	V2009	V20031	V891	V8112
	V2005	V2044	V2033	V892	V2790
	V3531	V2049	V2037	V905	
		V2315	V2760	V1091	
	V2761	V2435		V1816	V2050
	V2041	DTA124EKA	DTA134TK	V2220	V2061
	V2046	V2782	V1570V1573	V2430	V2053

